



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Whitman et al.

**Serial No.:** 09/944,230

**Filed:** August 30, 2001

**For:** SPIN COATING FOR MAXIMUM  
FILL CHARACTERISTIC YIELDING A  
PLANARIZED THIN FILM SURFACE

**Confirmation No.:** 2488

**Examiner:** T. Dickey

**Group Art Unit:** 2826

**Attorney Docket No.:** 2269-4294.1US  
(98-1208.01/US)

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

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P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO/SB/08 be considered by the Examiner and made of record. Copies of any cited foreign patents, publications, or pending unpublished U.S. applications are enclosed pursuant to 37 C.F.R. § 1.98(a)(2).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

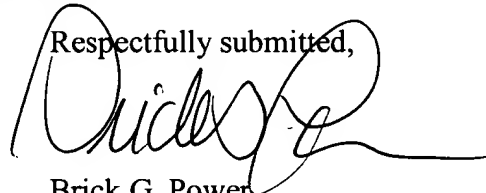
Other Documents

VAN ZANT, Peter, "Microchip Fabrication, A Practical Guide to Semiconductor Processing," pp. 176-178 and 185-187.

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application, and therefore no fee is due.

Respectfully submitted,



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BGP/ps:eg

Enclosures: Form PTO/SB/08  
Cited Non-U.S. Patent Document

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PTO/SB/08B(10-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

**Complete if Known**

Application Number	09/944,230
Filing Date	August 30, 2001
First Named Inventor	Whitman et al.
Group Art Unit	2826
Examiner Name	T. Dickey
Attorney Docket Number	2269-4294 11US (98-1208 01/US)

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		VAN ZANT, Peter, "Microchip Fabrication, A Practical Guide to Semiconductor Processing," pp. 176-178 and 185-187.	

Examiner  
SignatureDate  
Considered

<sup>1</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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